

EXPRESS MAIL LABEL NO.: EV 4858070900 US

Form PTO-1449

Sheet 1 of 4

Applicant: Hofmeister, et al.

Serial No.: 10/697,395

Att'y Docket No.: 15436.247.11.1

Filing Date: October 30, 2003

Group: 2661

Confirmation No.: 7465

For: AUTOMATIC SELECTION OF DATA RATE FOR OPTOELECTRIC DEVICES

SUPPLEMENTAL INFORMATION DISCLOSURE CITATIONS MADE BY APPLICANT

U.S. Patent Documents

<u>Examiner Initial*</u>	<u>Document Number</u>	<u>Issue/Publication Date</u>	<u>Name</u>
<u>KJM</u> 1	5,854,704	12/29/1998	Grandpierre
<u>KJM</u> 2	6,256,127 B1	07/03/2001	Taylor
<u>KJM</u> 3	4,932,038	06/05/1990	Windus
<u>KJM</u> 4	2002/0021468 A1	02/21/2002	Kato et al.
<u>KJM</u> 5	2002/0060824 A1	05/23/2002	Liou et al.
<u>KJM</u> 6	2003/0053170 A1	03/20/2003	Levinson et al.

Foreign Patent Documents

NONE

Examiner:

*John J. Chalmers*

Date Considered:

*11/7/06*

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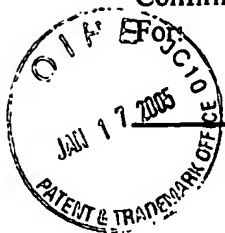
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Other Documents

(including author, title, pertinent pages, etc.)

Examiner

Initial\*

- KJM 7 *LXT16706/16707 SerDes Chipset*, Intel Products, [www.intel.com/design/network/products/optical/phys/lxt16706.htm](http://www.intel.com/design/network/products/optical/phys/lxt16706.htm), April 19, 2002.
- KJM 8 *LXT35401 XAUI-to-Quad 3.2G Transceiver*, Intel Products, [www.intel.com/design/network/products/optical/phys/lxt35401.htm](http://www.intel.com/design/network/products/optical/phys/lxt35401.htm), April 19, 2002.
- KJM 9 Texas Instruments User's Guide, *TLK2201 Serdes EVM Kit Setup and Usage*, Mixed Signal DSP Solutions, July 2000.
- KJM 10 Texas Instruments User's Guide, *TLK1501 Serdes EVM Kit Setup and Usage*, Mixed Signal Products, June 2000.
- KJM 11 National Semiconductor DS92LV16 Design Guide, *Serializing Made Simple*, February 2002.
- KJM 12 Vaishali Semiconductor, *Fibre Channel Transceiver*, VN16117, MDSN-0002-02, 08/09/2001.
- KJM 13 Fairchild Semiconductor, Application Note 77, *CMOS, the Ideal Logic Family*, January 1983.

Examiner: *KJM [Signature]*

Date Considered: *1/7/06*

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KJM

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Analog Target Specification, Annex 48B, Published by IEEE New York, 05/2001,  
pp. 6-14.

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### References Cited by Applicants

While the filing of Information Disclosure Statements is voluntary, the procedure is governed by the guidelines of Section 609 of the Manual of Patent Examining Procedure and 37 C.F.R. §§ 1.97 and 1.98. To be considered a proper Information Disclosure Statement, Form PTO-1449 shall be accompanied by a copy of each listed patent or publication or other item of information and a translation of the pertinent portions of foreign documents (if an existing translation is readily available to the applicant), an explanation of relevance of each reference not in the English language, and should be submitted in a timely manner as set out in MPEP Sec. 609.

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The reference designations "A1," "A2," etc. (referring to Applicant's reference 1, Applicant's reference 2, etc.) will be used by the Examiner in the same manner as Examiner's reference designations "A," "B," "C," etc. on Office Action Form PTO-1142.

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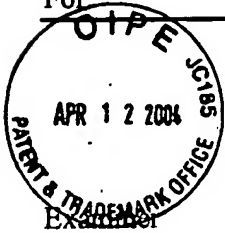
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Art Unit: 2661

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Foreign Patent Documents

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Prior Applications Filed

(including serial number, title, filing date, etc.)

<u>Examiner Initial*</u>	<u>Serial No.</u>	<u>Date</u>	<u>Title</u>
<i>KM</i> 1	10/629,301	07/28/2003	Transceiver Module and Integrated Circuit With Multi-Rate Eye Openers and Bypass

**References Cited by Applicants**

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